Problem #1: Digital CMOS inverter can be approximated as a circuit given in the figure.

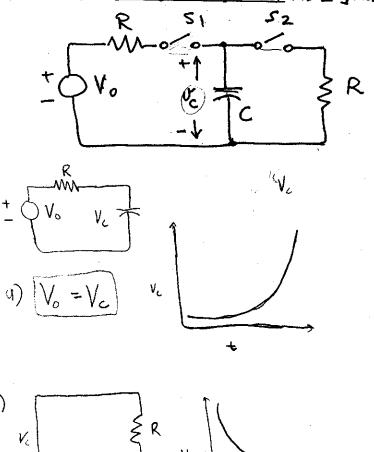
(a.) find the expression for the voltage V_c (as a function of time t) when switch S1 is ON and S2 is off. Assume that the capacitor C was completely discharged before S1 was turned ON. Draw a graph of V_c (4)

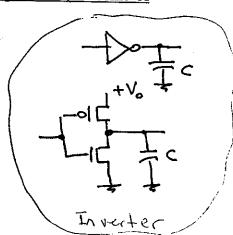
Quiz -4

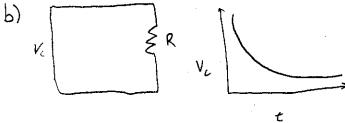
(b.) find the expression for the voltage V_c (as a function of time t) when (after sufficient time has elapsed to charge the capacitor C fully) the switch S1 is turned off and switch S2 is turned on. Draw - graph: $V_c(t)$

Note: this is the first grade college physics (Sears & Zemansky) problem (for some of

you perhaps a high-school problem). (12 1 graze)







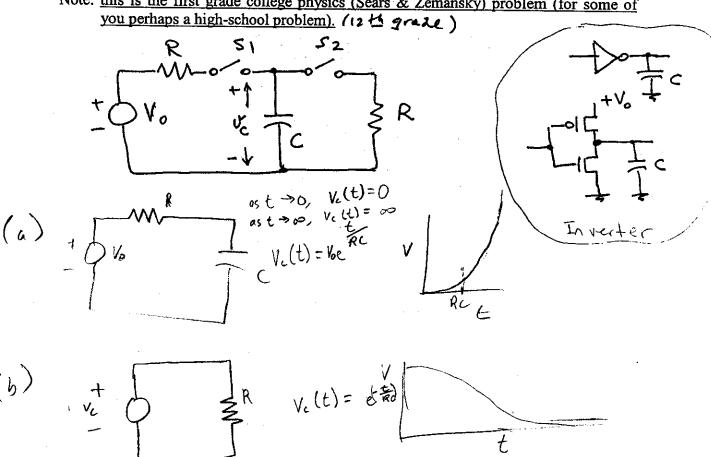
Problems:



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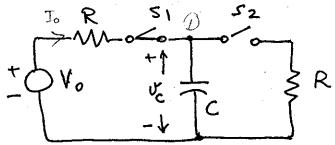


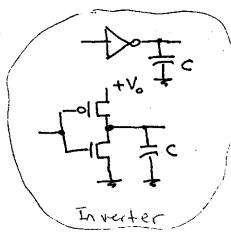
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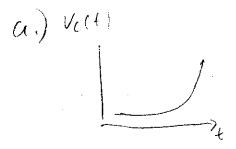
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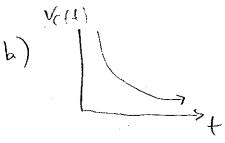
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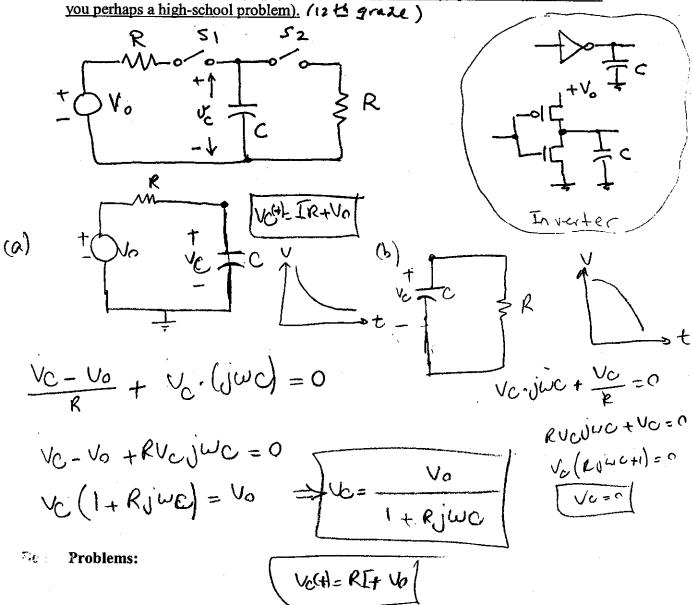


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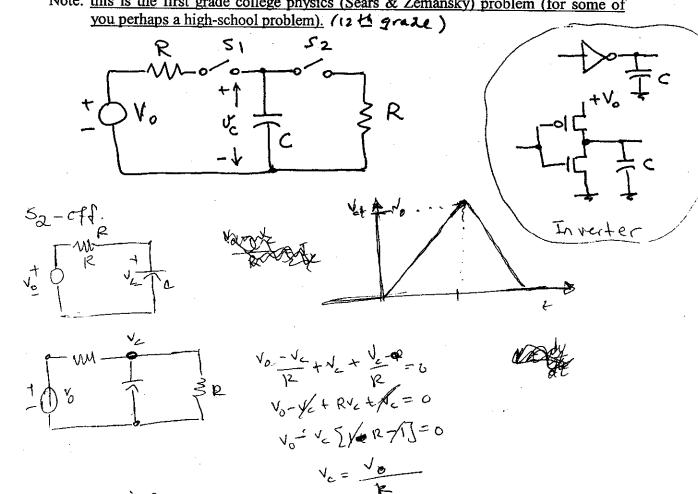
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(1)

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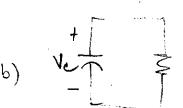
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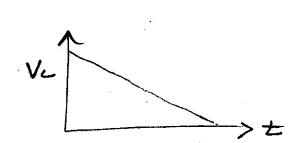
when SI is on circuit becomes



Ve



 $V_{L}(t) = \frac{V_{O}}{Kt}$



D

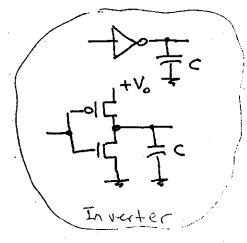
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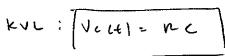
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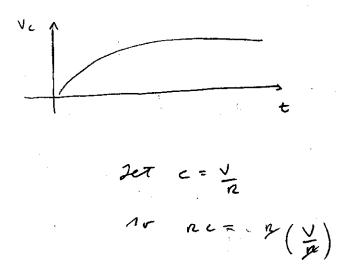
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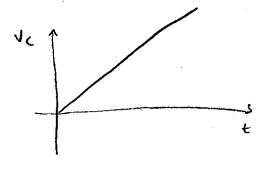
Note: this is the first grade college physics (Sears & Zemansky) problem (for some of



KUL; Velt1 = Vo + RC







Problems:

2)

· V

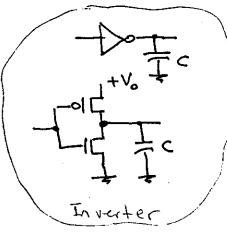


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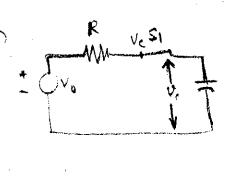
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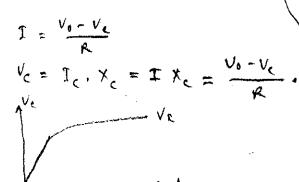
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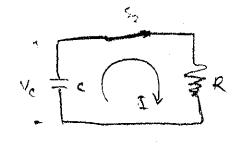


t



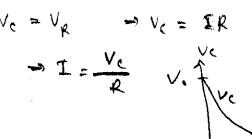


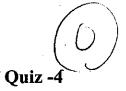
As time chapse, ve cherrenses percourse voltage usual drop on R.



ि : Problems:

b)





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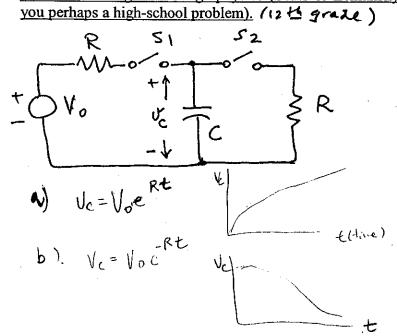
off and switch S2 is turned on. Draw a graph: Velt) Note: this is the first grade college physics (Sears & Zemansky) problem (for some of you perhaps a high-school problem). (12 1 grave) 51 R SI De ... Problems: = RCdVc+ Ve



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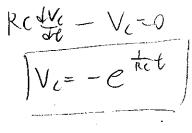
you perhaps a high-school problem). (12 1 graze) 51

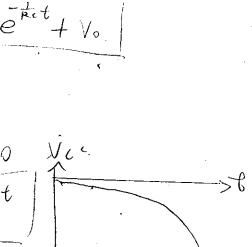
(M)

$$V_c = |s_1s_2| + |s_1s_2|$$

Vc = Vo - IR

 $V_c = \Lambda R = R c \frac{W}{di}$



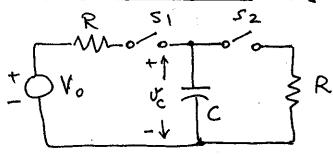


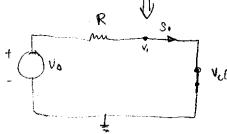
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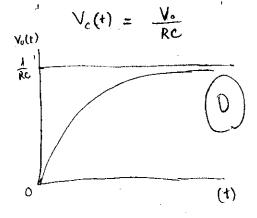
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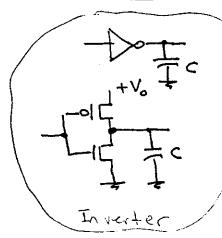


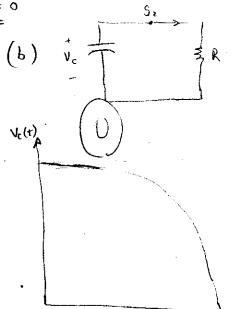
 $V_{c(t)}$ This is RC circuit where t=0 => $V_{c}(t)=0$ when $t=\infty$ => $V_{c}(t)$ =



De : Problems:

(a)





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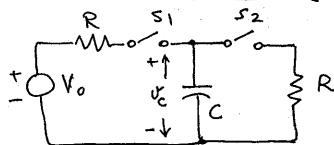
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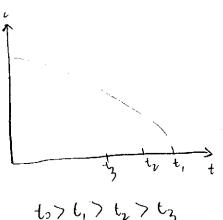
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t 37 tr 74, 7 to b)

Via IS OFF

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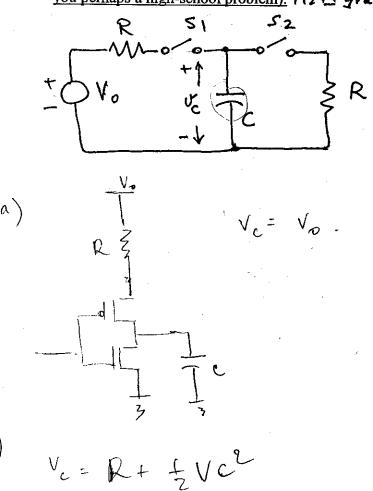


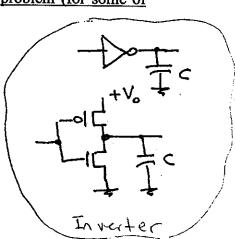
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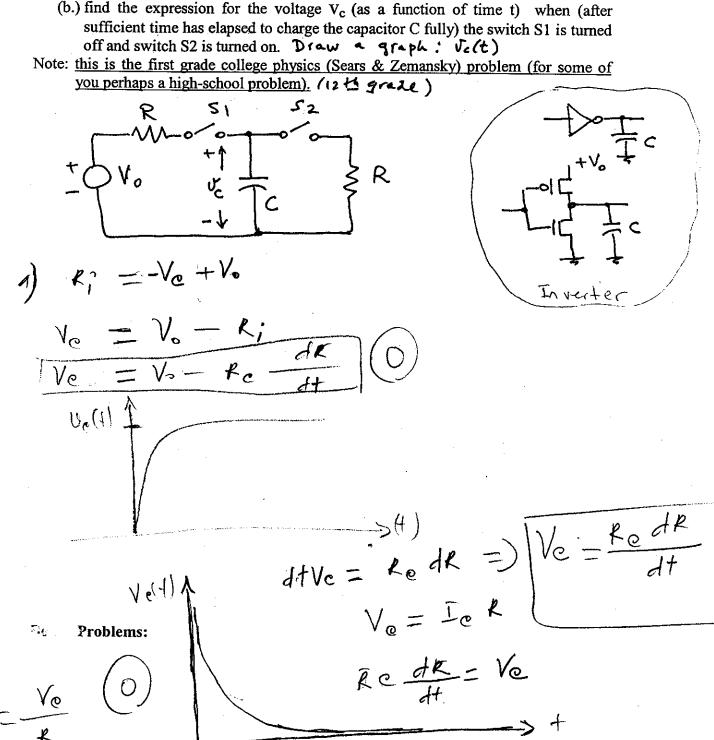
Problems:

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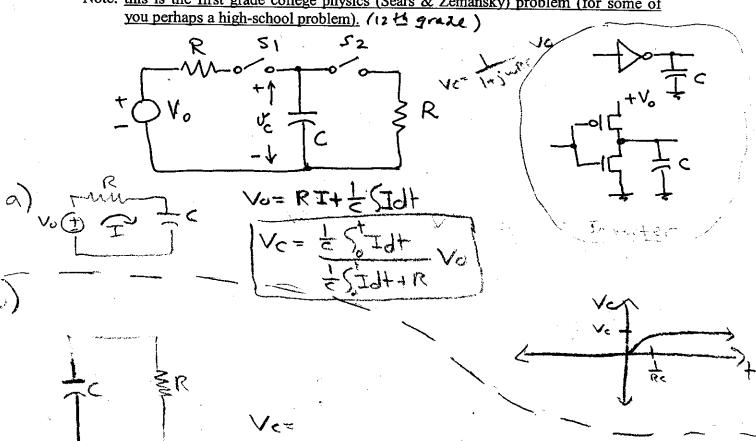
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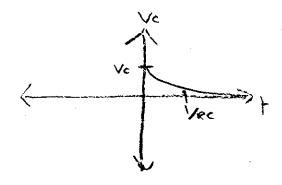
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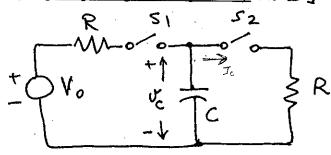


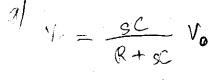
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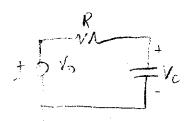
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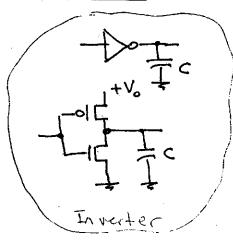
you perhaps a high-school problem). (12 1 grate)





v(3)= Vc+ 1/2





Ve =

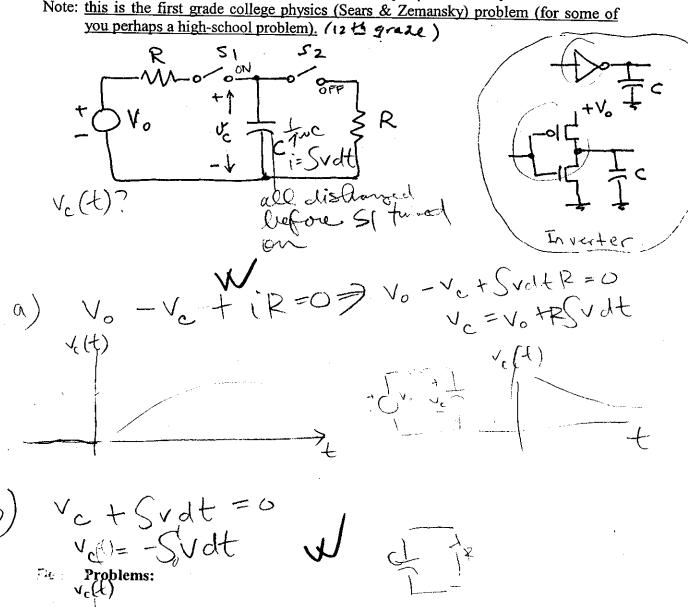


$$V = I_{c}R$$

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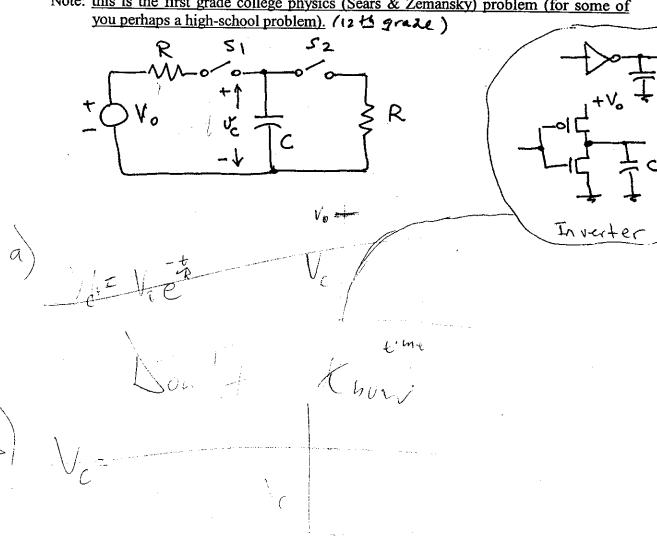
Problems:

Your Name:

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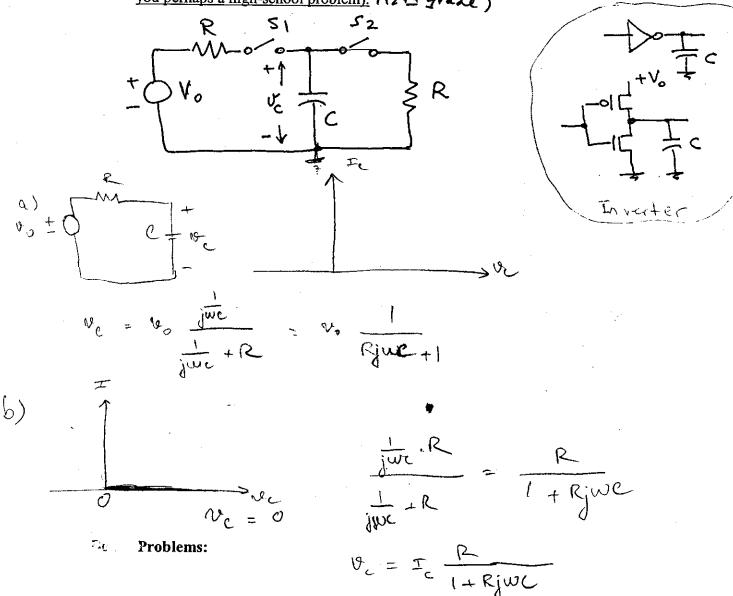
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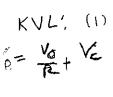


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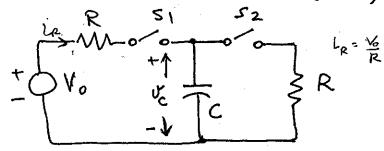
KVL (2);

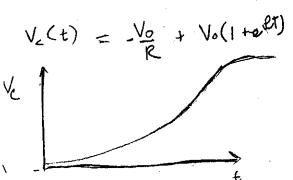
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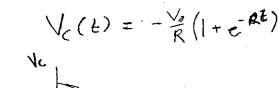
Vc = - Vc

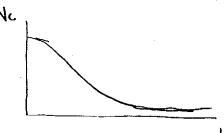
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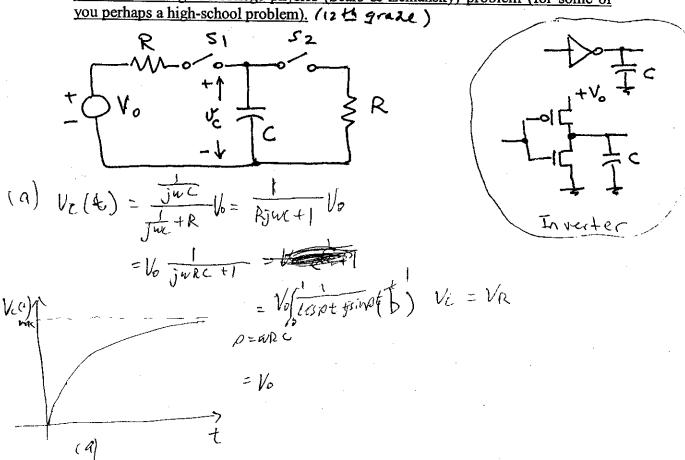




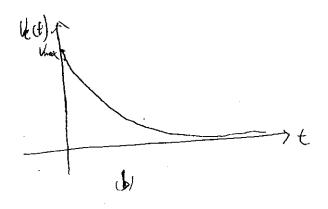
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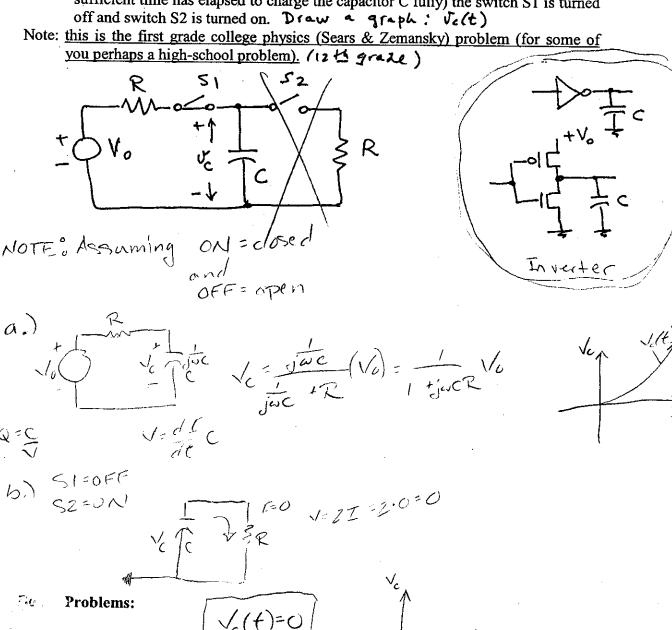


Te Problems:



Problem #1: Digital CMOS inverter can be approximated as a circuit given in the figure.

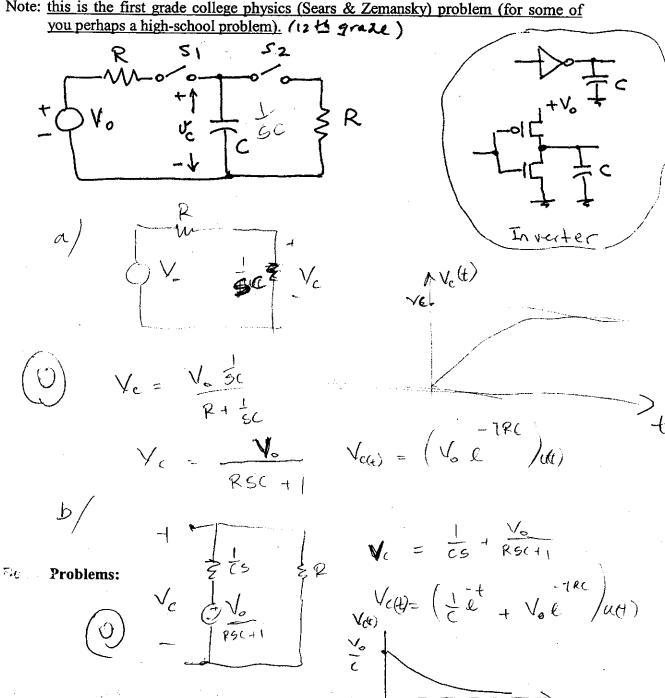
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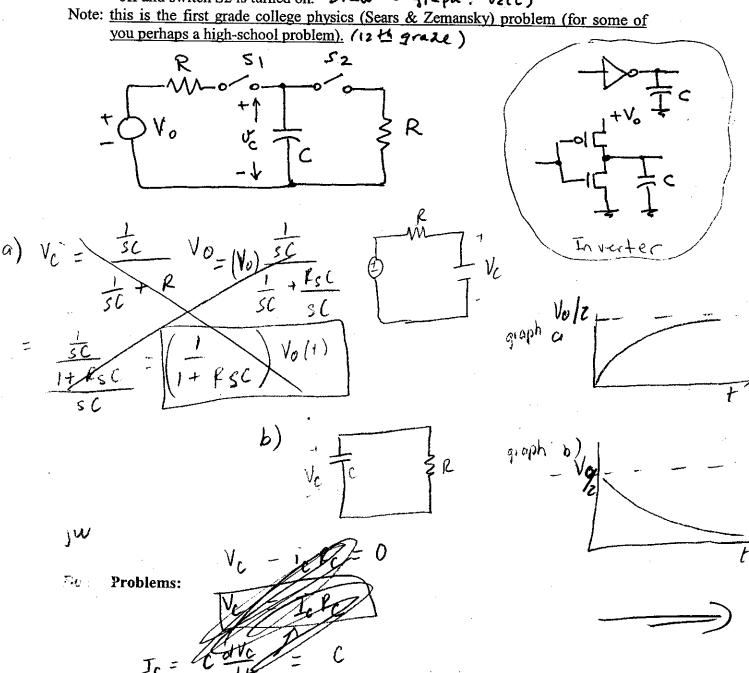
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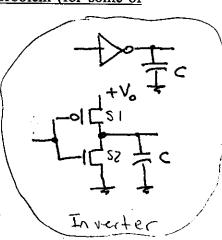
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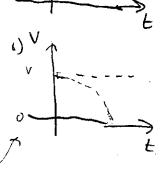
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 $V_{0}(t) = R + 1$ $V_{0}(t) = R_{j} \omega C + 1$ $V_{0} j \omega C = R_{j} \omega C + 1$ $j \omega C (V_{0} - R) = 1$ $j \omega C = 1$ $V_{0} - R$ $W = \left(\frac{1}{V_{0} - R}\right) \frac{1}{C_{j}}$ $V_{c} = R + \frac{1}{J} \omega C$



 $\omega = \left(\frac{1}{v_c - e}\right)^{\frac{1}{c}}$

Problems: Note to TAS this whole quiz is an UNREASONABLE

1'udge of our knowledge of DIGITAL CIRCUITS

why should we need to know and one log. I'm a

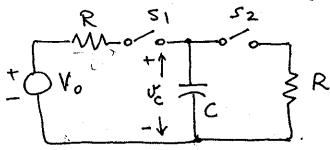
Conjuter Science Student This is an EE course and this
is a quiz of basic knowledge. You should complain about your degree.

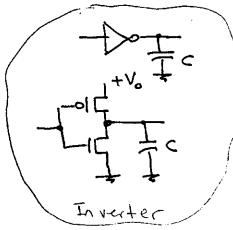
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a) voti it Teve



b) on back

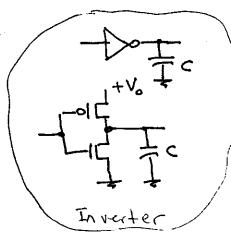


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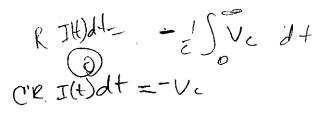
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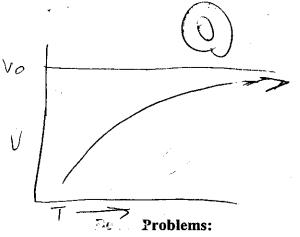
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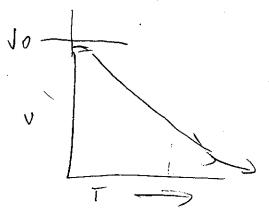
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 $\frac{1}{c} \int_{0}^{\infty} V_{c} - V_{o} = J(t)R$ $V_{c} = RCJ(t)dt$



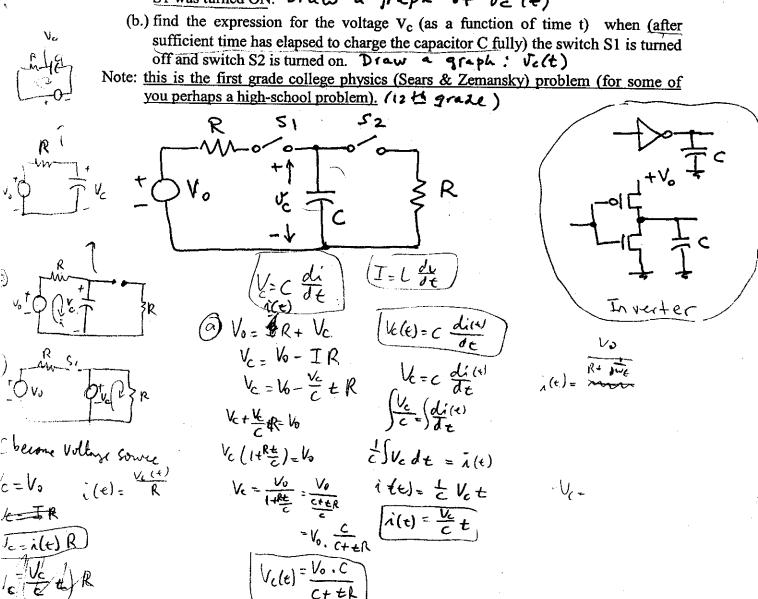




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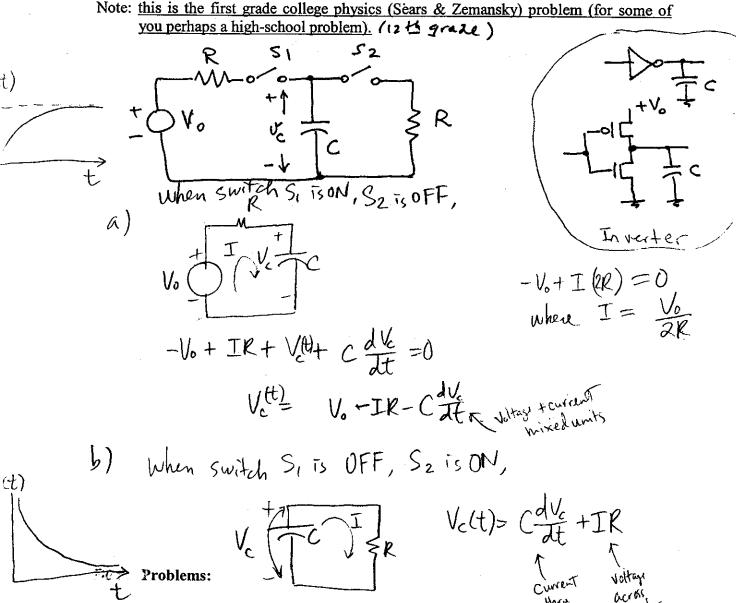
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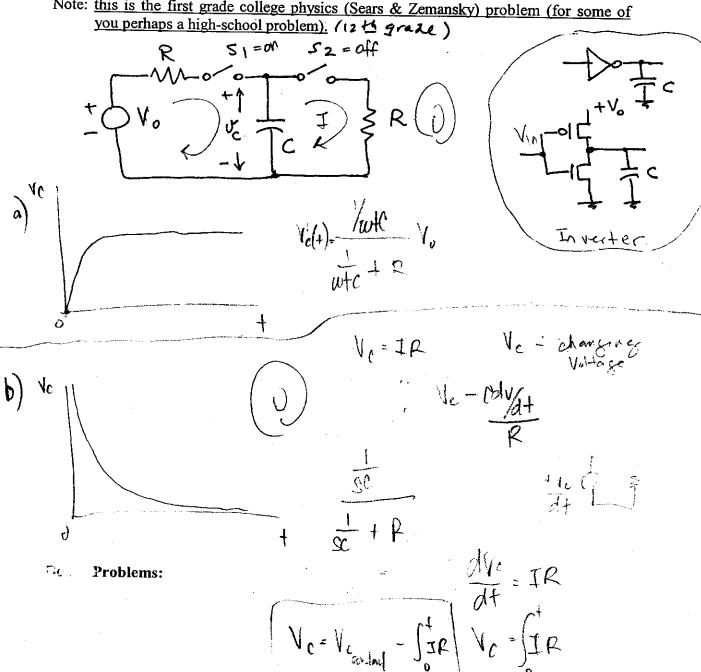
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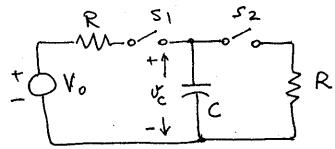


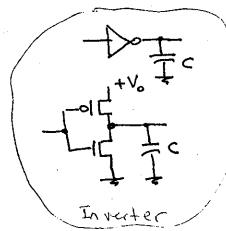
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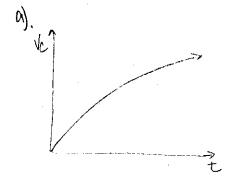
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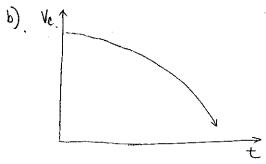
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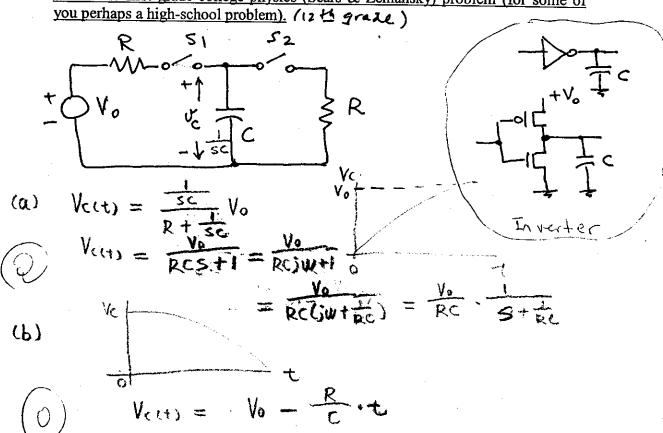
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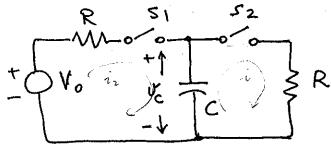


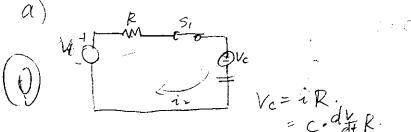
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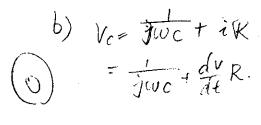
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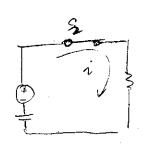




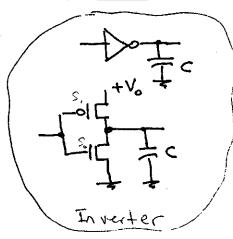
$$V_4-V_c+i_zR=0$$

$$V_4=-i_zR+V_c=\overline{jwc}-i_zR$$





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Vt=16 (S,=

V4= 0 (S,=01)

 Σ_{ij} Problems: