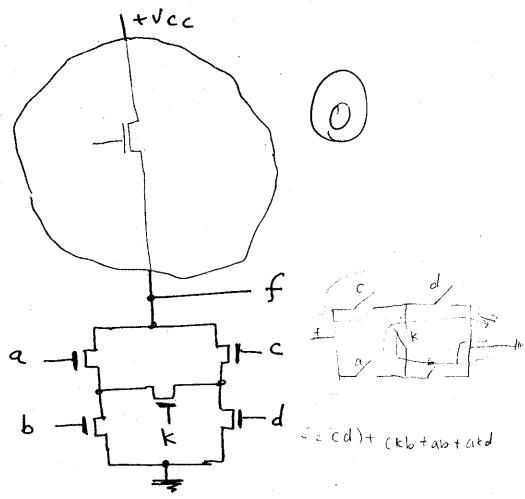


Problem #1:

Complete the circuit diagram for the given CMOS logic block.

You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.

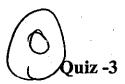
(a)



(b) Write the logical expression for f.

f= cd + ckb + ab + akd = F



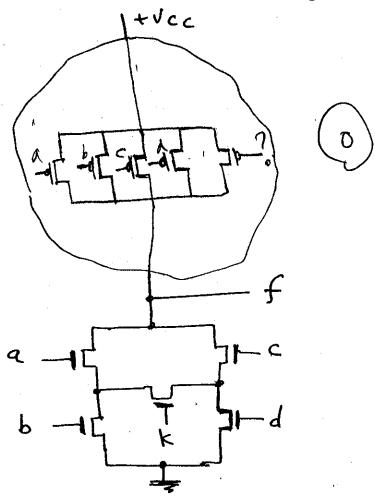


Problem #1:

Complete the circuit diagram for the given CMOS logic block.

You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.

(a)



(b) Write the logical expression for f.

f = akcbd

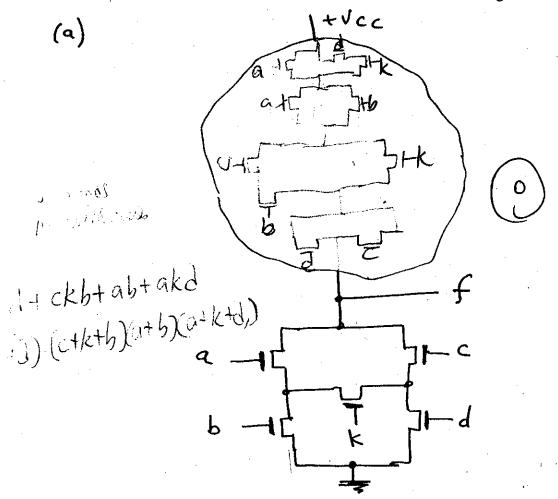


Vour	Name:
JUUL	T JOHN TO

Problem #1:

Complete the circuit diagram for the given CMOS logic block.

You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.



(b) Write the logical expression for f. f=(z+d)(C+k+b), (a+b)(a+k+d)(0)

Problem #1:

Complete the circuit diagram for the given CMOS logic block.
You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.

Comes

No Ptype

South that

Correctly is Minimum

When I is biased

C= + Vcc

Where 2 is biased

A = 0

(b) Write the logical expression for f.

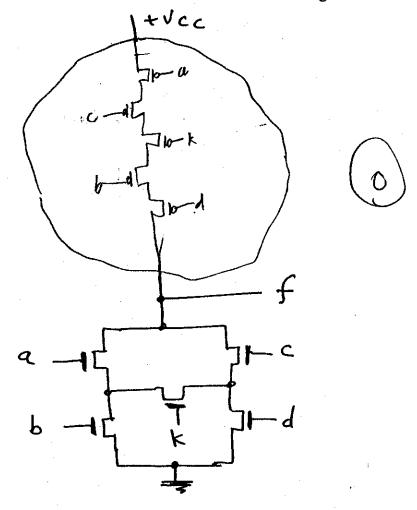
f = ab + cd + akd + bck = f

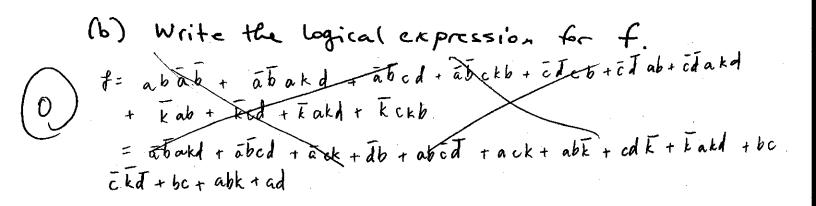
Problem #1:

Complete the circuit diagram for the given CMOS logic block.

You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.

(a)



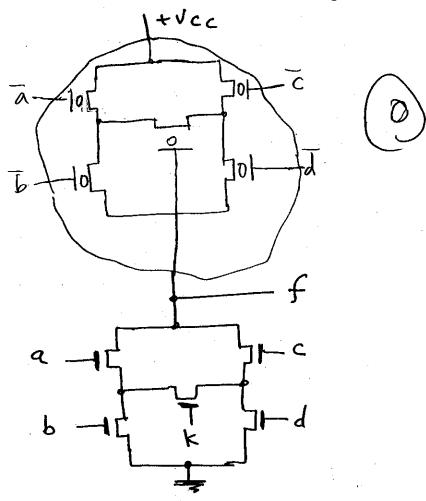


Problem #1:

Complete the circuit diagram for the given CMOS logic block.

You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.

(a)

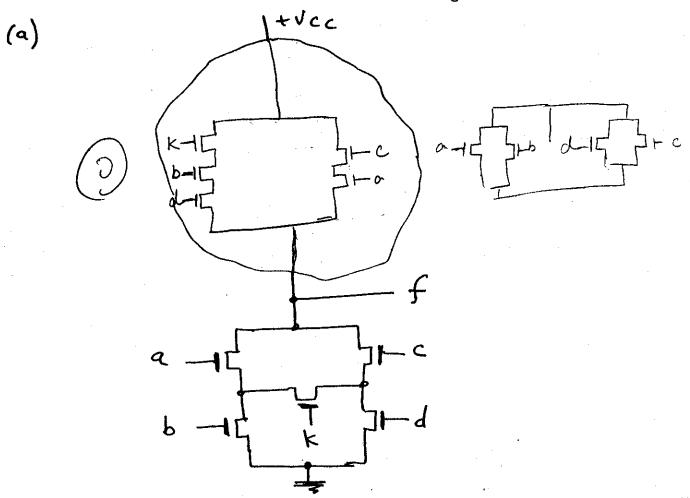


(b) Write the logical expression for f. $f = pMOS \cdot nMOS = (wx + yz) \cdot (ab + cd)$ $= (\overline{ab} + \overline{cd}) \cdot (ab + cd) = (\overline{ab} \cdot \overline{cd}) \cdot (ab + cd)$ $= (\overline{ab} + \overline{cd}) \cdot (ab + cd) = (\overline{ab} \cdot \overline{cd}) \cdot (ab + cd)$ $= (a+b) \cdot (c+d)$ $= (a+b) \cdot (c+d)$



Problem #1:

Complete the circuit diagram for the given CMOS logic block. You can treat transistors as switches as explained in the class. Use the methods that you learned in lectures. Your final circuits should be in the minimal logic form.



(b) Write the logical expression for f. $f = (b + d + k) \cdot (c + a)$