

# Nanostructure Integration Techniques for Manufacturable Devices, Circuits and Systems: Interfaces, Interconnects, and Nanosystems (SA112)

Part of SPIE's International Symposium on Optics East 2005  
23-26 October 2005 • Boston Marriott Copley Place • Boston, MA, USA

Conference Chairs: **Minoru M. Freund**, Air Force Research Lab.; **M. Saif Islam**, Univ. of California/Davis; **Achyut K. Dutta**, Banpil Photonics, Inc.

Program Committee: **A. F. Mehdi Anwar**, Univ. of Connecticut; **Jeyadevan Balachandran**, Tohoku Univ. (Japan); **Mehmet Bayindir**, Massachusetts Institute of Technology; **Gijs Bosman**, Univ. of Florida; **Yong Chen**, Univ. of California/Los Angeles; **Salim Ciraci**, Bilkent Univ. (Turkey); **Nuh Gedik**, California Institute of Technology; **Martina Gerken**, Univ. Karlsruhe (Germany); **Anisul Haque**, Bangladesh Univ. of Engineering and Technology (Bangladesh); **Zameer U. Hasan**, Temple Univ.; **David A. Horsley**, Univ. of California/Davis; **Ahalapitiya H. Jayatissa**, Univ. of Toledo; **Ian Kennedy**, Univ. of California/Davis; **Nobuhiko P. Kobayashi**, Univ. of California/Berkeley; **Jing Li**, NASA Ames Research Ctr.; **Marko Loncar**, Harvard Univ.; **Gilberto Medeiros-Ribeiro**, Lab. Nacional de Luz Síncrotron (Brazil); **Hou T. Ng**, Hewlett-Packard Labs.; **Robert Olah**, Banpil Photonics, Inc.; **Sharka M. Prokes**, Naval Research Lab.; **Ant Ural**, Univ. of Florida; **Kang L. Wang**, Univ. of California/Los Angeles; **Shih-Yuan Wang**, Hewlett-Packard Labs.; **Richard T. Webster**, Air Force Research Lab.; **Dwight L. Woolard**, Army Research Lab.; **Ming C. Wu**, **Peidong Yang**, Univ. of California/Berkeley; **Chongwu Zhou**, Univ. of Southern California

Keynote Presentation by **Lars Samuelson**, Lunds Univ.

Nanotechnology promises the convergence of nano-electronics, nano-photonics, NEMS & MEMS structures, and other sensor components into single "intelligent" monolithic devices, possibly in 3D. However, despite significant progress in nanostructure synthesis (quantum dots, nanowires, etc.) and many promising single device demonstrations (such as FETs made with carbon nanotubes or nanowires) there are significant hurdles to realize such nanodevices, such as inability to controllably incorporate nano-sized components within integrated circuits, and to manufacture such devices with 5N or 6N reliability.

This conference will consider existing and new integration methods of nano-scale structures and devices with other nano-, micro-, and macro-scale electronic devices and circuits, as well as development of processes and fabrication techniques for nanoscale devices and electronics. The objective of this conference is to bring together experimentalists, theorists, computational specialists, and development engineers to provide an interdisciplinary forum to discuss the state-of-the-art of nano-materials and devices integration techniques. Areas of research that are particularly active include massively parallel techniques for the growth, fabrication and characterization of nano-structures, nanowires, nano-bridges, nanotubes, quantum dots, quantum wires, DNA, molecules and wafer level integration techniques. Furthermore, we must consider fast, reliable electrical testing of large numbers of nanostructures without resorting to nanoprobe or tedious and expensive serial interfacing. Several groups have developed/reported unique approaches of massively parallel 'in situ' techniques to integrate nanodevices in different semiconductor, metal and bio nano-material systems. Novel nano-electronic and nano-photon devices are being designed and studied with a goal of making them an integral part of future nano-systems. Issues such as growth conditions, doping techniques and manufacturable wafer processing methods are among those that need more research.

This special meeting will be of interest to researchers in nano-sciences and technology. We hope to bring together researchers from a wide field of interest that include materials science, optics, physics, chemistry, biology, electrical engineering, etc.

## 1. Integration of 3D Confined Structures

- Synthesis of quantum dots, nano-particles, 3D confined structures in various material systems
- Templating and controlled growth.
- Massively parallel fabrication and integration of 3D confined structures in circuits and systems.
- Advanced topics including physical characteristics of nanoparticles as an individual and as an ensemble, precise positioning, electrical and optical characterization techniques.

## 2. Semiconductor Nanowires for Manufacturable Devices and Circuits: Synthesis, Doping, Device Fabrication and Integration

- Novel nanowire based assemblies.
- Manufacturable techniques for large array nanowire devices such as sensors, transistors, decoders and interconnects for nano-electronics.
- Material and process issues with interfacing nano-scale devices.

- Massively parallel fabrication of nanowire devices.
- Precursors, catalysts and contact materials for compatible integrations, doping, and in-situ synthesis techniques.
- Special orientation of materials and substrates for nano-synthesis.

## 3. Heterogeneous Integration

- Heterogeneous synthesis of quantum dots and wires in different material systems.
- Physics, theory, simulation, and modeling: theoretical limits of functional integrations.
- Fabrication of nanodevices with heterogeneous materials: integration and applications issues.

## 4. Bioelectronic Structures and Integration

- Electronic and photonic devices with biomolecules, DNA etc.
- Biologically-assisted massively parallel nanofabrication.
- New devices and systems that are hybrids of traditional polymeric and semiconductor materials with biological materials.
- Single cell measurement with semiconductor and metallic nanostructures for disease diagnostics.
- Bio-compatible nano-materials: requirements and current trends.
- Bio-electronic interfaces.

## 5. Characterization Techniques for Integrated Nanostructures

- Characterization techniques for interface evaluation, DC, RF and high frequency noise, high speed characterization of nanostructures.
- Characterization of individual and an ensemble of nano-structures.
- Novel photonic techniques for characterization.

## 6. Integration of Nanotubes

- Novel nanotube growth and synthesis techniques for manufacturable devices and circuits.
- Integration of nanotubes with conventional devices and circuits.
- Evaluation of nanotube-substrate interfaces, structural and electrical characteristics of interface with metals, existing issues and integration of nanotubes with silicon microfabrication processes.

## 7. Advanced Topics in Nano-integrations

- 3D integration of nano-devices and circuits.
- Integrated systems with molecular logics and switching devices.
- Integrated nanoemitter and receiver.
- Nanostructure growth conditions, novel doping techniques and manufacturable wafer processing methods.
- Application of advanced patterning techniques for precise positioning and dimension control of nanostructures.
- Photonic crystal integration with active and passive devices.
- Synthesis of new materials: semiconductors, dielectrics, polymers, superconductors, organics, magnetics, pyroelectrics, etc.
- Novel characterization techniques for evaluating the optical, electrical, structural performance of integrated nanodevices.
- Nanodevice integration with NEMS and NOEMS devices.
- Nanodevices for in-situ bio-medical applications.

Abstract Due Date: 11 April 2005

Manuscript Due Date: 26 September 2005

# Submission of Abstracts for *Optics East 2005* Symposium

Abstract Due Date: 11 April 2005

Manuscript Due Date: 26 September 2005

## **IMPORTANT!**

**Submissions imply the intent of at least one author to register, attend the symposium, present the paper (either orally or in poster format), and submit a full-length manuscript for publication in the conference Proceedings.**

All authors (including invited or solicited speakers), program committee members, and session chairs are responsible for registering and paying the reduced author, session chair, program committee registration fee. (Current SPIE Members receive a discount on the registration fee.)

## **Instructions for Submitting Abstracts via Web**

You are **STRONGLY ENCOURAGED** to submit abstracts using the "submit an abstract" link at:

<http://spie.org/events/oe>

**Submitting directly on the Web ensures that your abstract will be immediately accessible by the conference chair for review through MySPIE, SPIE's author/chair web site.**

Please note! When submitting your abstract you must provide contact information for all authors, summarize your paper, and identify the **contact author** who will receive correspondence about the submission and who must submit the manuscript and all revisions. Please have this information available before you begin the submission process.

First-time users of MySPIE can create a new account by clicking on the [create new account](#) link. You can simplify account creation by using your SPIE ID# which is found on SPIE membership cards or the label of any SPIE mailing.

If you do not have web access, you may E-MAIL each abstract separately to: [abstracts@spie.org](mailto:abstracts@spie.org) in [ASCII text \(not encoded\)](#) format. There will be a time delay for abstracts submitted via e-mail as they will not be immediately processed for chair review.

**IMPORTANT!** To ensure proper processing of your abstract, the SUBJECT line must include only:

**SUBJECT: SA112, FREUND, ISLAM, DUTTA**

**Your abstract submission must include all of the following:**

- 1. PAPER TITLE**
- 2. AUTHORS** (principal author first) For each author:
  - First (given) Name (initials not acceptable)
  - Last (family) Name
  - Affiliation
  - Mailing Address
  - Telephone Number
  - Fax Number
  - Email Address
- 3. PRESENTATION PREFERENCE** "Oral Presentation" or "Poster Presentation."
- 4. PRINCIPAL AUTHOR'S BIOGRAPHY** Approximately 50 words.
- 5. ABSTRACT TEXT** Approximately 250 words.
- 6. KEYWORDS** Maximum of five keywords.

## **Accepted Abstracts**

A CD-ROM of accepted abstracts will be distributed to attendees onsite. Please submit only 250-word abstracts that are suitable for publication.

## **Conditions of Acceptance**

- Authors are expected to secure funding for registration fees, travel, and accommodations, independent of SPIE, through their sponsoring organizations before submitting abstracts.
- Only original material should be submitted.
- Commercial papers, papers with no new research/development content, and papers where supporting data or a technical description cannot be given for proprietary reasons will not be accepted for presentation in this symposium.
- Abstracts should contain enough detail to clearly convey the approach and the results of the research.
- Government and company clearance to present and publish should be final at the time of submittal. If you are a DoD contractor, allow at least 60 days for clearance. Authors are required to warrant to SPIE in advance of publication of the Proceedings that all necessary permissions and clearances have been obtained, and that submitting authors are authorized to transfer copyright of the paper to SPIE.

## **Review, Notification, Program Placement**

- To ensure a high-quality conference, all abstracts and Proceedings manuscripts will be reviewed by the Conference Chair/Editor for technical merit and suitability of content. Conference Chair/Editors may require manuscript revision before approving publication, and reserve the right to reject for presentation or publication any paper that does not meet content or presentation expectations. SPIE's decision on whether to accept a presentation or publish a manuscript is final.
- Applicants will be notified of abstract acceptance by mail no later than 1 August 2005. Early notification of acceptance will be emailed to authors the week of 20 June 2005.
- Final placement in an oral or poster session is subject to the Chairs' discretion. Instructions for oral and poster presentations will be included in your author kit. All oral and poster presentations are included in the *Proceedings of SPIE*, and require presentation at the meeting and submission of a manuscript.

## **Proceedings of SPIE**

- These conferences will result in full-manuscript Chairs/Editor-reviewed volumes published in the *Proceedings of SPIE*.
- Correctly formatted, ready-to-print manuscripts submitted in English are required for all accepted oral and poster presentations. Electronic submissions are recommended, and result in higher quality reproduction. Submission must be provided in PostScript created with a printer driver compatible with SPIE's online Electronic Manuscript Submission system. Instructions are included in the author kit and from the "Author Info" link at the conference website.
- Authors are required to transfer copyright of the manuscript to SPIE or to provide a suitable publication license.
- Papers published are indexed in leading scientific databases including INSPEC, Ei Compendex, Chemical Abstracts, International Aerospace Abstracts, Index to Scientific and Technical Proceedings and NASA Astrophysical Data System.
- **Late manuscripts may not be published in the conference Proceedings**, whether the conference volume will be published before or after the meeting. The objective of this policy is to better serve the conference participants as well as the technical community at large, by enabling timely publication of the Proceedings.
- **Papers not presented at the meeting will not be published in the conference Proceedings**, except in the case of exceptional circumstances at the discretion of SPIE and the Conference Chairs/Editors.