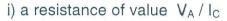
1.

a) Select and circle the one best answer:

The approximate small-signal model for a diode-connected transistor is



ii) a resistance of value $\beta V_T / I_C$



b) Select and circle the one best answer:

A current mirror:



ii) forces its input and output currents to be nearly equal

iii) uses one NPN and one PNP transistor



c) If a differential amplifier has $v_{in1} = 100 \text{mV} \cdot \cos(10 \text{t})$ and v_{in2} = 200mV·cos(10t), what are the Common Mode Input Voltage (v_{ic}) and the Differential Mode Input Voltage (vid)?

$$V_{ic} = \frac{150 \,\text{mV} \, \cos \left(10 \,t\right)}{V_{in_1} + V_{in_2}}$$

$$V_{in_1} - V_{in_2}$$

$$V_{in_1} - V_{in_2}$$

$$V_{id} = \frac{-100 \text{ mV cas} (10 + 1)}{11}$$

$$V_{in_1} - V_{in_2}$$

2. In the circuit below, R_{in} and R_{out} have been measured in the lab: R_{in} = 4k ohms and R_{out} = 112k ohms. Assuming the transistors is forward active and the DC collector current is 1 mA:

What is β for this transistor? $\beta = 154$

What is V_A for this transistor? $V_A = \frac{112 \text{ V}}{}$

$$R_{in} = F = \frac{\beta V_T}{I_C} = \frac{\beta (26mV)}{ImA}$$

$$4kR \Rightarrow \beta = 154$$

$$R_{out} = V_o = \frac{V_A}{I_C} = \frac{V_A}{ImA}$$

$$112kR \Rightarrow V_A = 112V$$

3. a) A current source is shown below. All transistors are identical. Assume Q2 is forward active (its collector connects to other circuitry that is not shown). Find the value of $I_{\rm O}$ in the model shown.

b) What is the minimum value of the voltage at the collector of Q2 (V_O) for which Q2 remains forward active?

$$\exists T_{c_3} = T_{c_4} = T_{c_2} = T_0 \text{ (ignoring } r_0)$$

$$T_{c_3} = T_{ref} = \frac{15V - 2V_{0E}}{10k \text{ N}}$$

$$= \frac{15V - 2(0.7V)}{10k \text{ N}}$$

b)
$$V_{E}(Q2) = V_{BE, +} V_{BE, 2} - V_{BE, 2} \simeq 0.7V$$

 $V_{O}(m.i.) \equiv 0.7V + V_{CES4T}(Q2) = 0.8V$



- 4. Find the DM and CM gains for the differential amplifier below. Assume all transistors are forward active; Q1 and Q2 are identical; and $I_{C1}(D.C.) = I_{C2}(D.C.) = 1$ mA. FOR THIS PROBLEM ONLY, ignore the transistor's output resistance (that is, take V_A = infinity).
- a) What is the Differential Mode (DM) gain?

$$v_{od}/v_{id} = -7.5$$

b) What is the Common Mode (CM) gain?

$$v_{oc}/v_{ic} = \underline{-5}$$
.

Vice

$$V_{01}$$
 V_{02}
 V_{03}
 V_{04}
 V_{05}
 V_{04}
 V_{05}
 V_{05}

Extra work space for Problem 4.

cm Eckt

$$a_{cm} = \frac{N_{cc}}{N_{ic}} = -G_{m_i}(50k) = \frac{-g_{m_i}}{1+g_{m_i}(10k)}(50k)$$

- 5. A class A output stage is shown below.
- a) What is the positive output swing limit? $V_{out}(max) = 9.9 \text{ V}$
- b) What is the negative output swing limit? $V_{out}(min) = -2.0 \text{ V}$

$$\frac{+10V}{\sqrt{2}}$$

$$\frac{10V}{\sqrt{2}}$$

$$\frac{1$$

$$\rightarrow$$
 $V_o(min) = -2V$

