Rule #1: Use only DFF, never latches.

Rule #2: For comb. logic, use only "=" in always block.

Rule #3: For FFE, always use "<".

Rule #4: Follow for 281. Add #1 delay for clk-to-Q.
Rule #5 - Avoid comb. logic & FF declarations

Rule #6 - Group logic & FFs together

// Block 1

// Logic

// FFs

// Block 2

// Logic

// FFs

...

Rule #7

a) Only clk signals connect to FF clock inputs
   c) There are a few exceptions

b) clks may go to any node other than
   FF clk input
Rule #8 - Use naming conventions for signals