The Static CMOS Inverter

- The simplest CMOS digital gate is the inverter
- $\text{out} = \text{NOT}(\text{in})$
- Logic symbol (top)

- Transistor schematic (bottom)
Here is an AND gate (with an inverted output, which is called a NAND)
2-Input Static CMOS NOR Gate

- Here is an OR gate (with an inverted output, which is called a NOR)

Source: Mike Lai
• Here is a Full Adder

![Full Adder Layout](image)

Source: Mike Lai