MULTI-PROJECT WAFER (MPW) ORGANIZATIONS
MOSIS

• Low-cost VLSI prototyping and small-volume production service
• Affiliated with Information Sciences Institute at USC in Marina del Rey, California
• Since 1981, they have fabricated more than 50,000 circuit designs for commercial firms, government agencies, and research and educational institutions
• Traditionally has been best place for support of magic and design rules
  – Changing somewhat recently
• MOSIS wrote magic’s design rules
Since 1981, CMP has been a non profit Multi-Project Wafer (MPW) service organization in ICs, Si-Photonics, 3D-ICs and MEMS, Smart Power, for prototyping and low volume production.

CMP: Circuits Multi-Projects

Located in Grenoble, France
A total of 265 circuits have been fabricated for 90 Institutions, Research Laboratories and Companies from 25 countries.

The nominal turnaround time, from the closing date of the run to the delivery of packaged chips was:

- CMOS 11 weeks
- HV CMOS 12 weeks
- SiGe BiCMOS 14 weeks
- Advanced CMOS 16 - 24 weeks

Including 1-2 weeks of data preparation and 2-3 weeks wafer dicing and packaging.

https://mycmp.fr/
Muse Semiconductor

- Muse Semiconductor is a relatively-new semiconductor company focused on providing MPW services to university researchers.
- MUSE is an acronym for MPW University Service
- Located in San Jose, CA and Denver, Colorado
- They offer TSMC exclusively
  - 180 nm
  - 65 nm
  - 40 nm
  - 28 nm
- https://www.musesemi.com/