

YOUR NAME HERE:

EEEC180A

DIGITAL SYSTEMS I

Spring 2000

1. Write logical expressions for functions F_0, F_1, F_2, F_3

(this is your textbook example, page 235)

MULTIPLE-OUTPUT NETWORKS

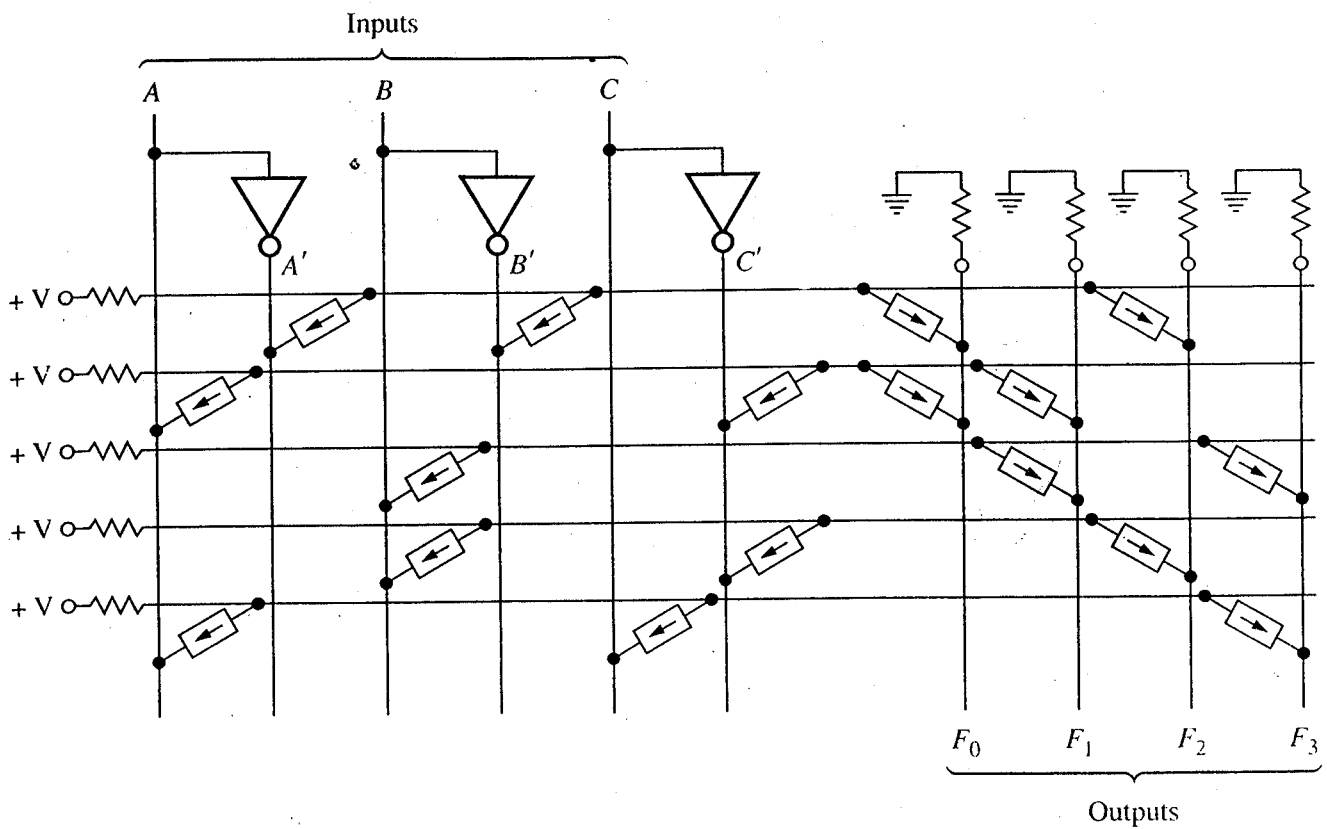


Figure 9-27
PLA with 3 Inputs, 5 Product Terms,
and 4 Outputs