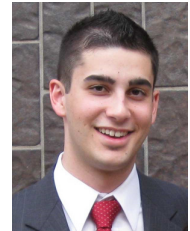


JON PIMENTEL



EDUCATION

Ph.D. Student, Electrical and Computer Engineering

University of California, Davis, September 2009 - Present 3.88/4.0 GPA

Bachelor of Science, Electrical Engineering

University of California, Davis, June 2009 3.7/4.0 GPA

WORK EXPERIENCE

UC Davis VLSI Computation Laboratory, Davis, CA

Graduate Student Researcher (2009 - present)

- Implementing parallel synthetic aperture radar application for 167 core chip
- Parallelizing electronic warfare radar application for low power multicore platform
- Researching biomedical imaging modalities and signal processing algorithms

UC Davis VLSI Computation Laboratory, Davis, CA

Undergraduate Researcher (2008 - 2009)

- Implemented RISC assembly programs to run on 36 core AsAP 1 chip
- Wrote PERL test scripts for measuring asynchronous processor array variation
- Measured, simulated and analyzed multi-core processor hardware and applications

SKILLS

Verilog, HSpice, Assembly (MIPS), MATLAB, PERL, Simulink, C, C++, HTML, UNIX Shell Script
Portuguese Speaker

RELATED COURSEWORK

- Computer Architecture
- High Performance Computer Architecture
- Signals & Systems I & II
- Signal Analysis & Communication
- Statistical & Digital Communications
- Linear Systems and Signals
- Low Power Digital Integrated Circuit Design
- VLSI Design
- VLSI Digital Signal Processing
- Digital Integrated Circuits
- Digital Systems I & II
- Digital System Testing
- Digital Signal Processing
- Embedded Computing Systems
- Biomedical Imaging Basic Principles
- Molecular Imaging
- Magnetic Resonance Imaging
- Medical Image Analysis
- Ultrasound Imaging

MEMBERSHIPS AND AWARDS

- Graduate Research Mentorship Fellow
- Golden Key International Honor Society
- Institute of Electrical & Electronics Engineers (IEEE)
- Design Automation Conference Expert Reviewer (2010, 2011)
- GAANN Fellow
- Tau Beta Pi Engineering Honor Society
- Phi Kappa Phi Honor Society

REFERENCES

Provided upon request

CITIZENSHIP STATUS

U.S. Citizen