

Paul J. Hurst was born in Chicago, IL in 1956. He received the B.S., M.S., and Ph.D. degrees in electrical engineering from the University of California at Berkeley in 1977, 1979, and 1983, respectively.

From 1983 to 1984, he was with the University of California, Berkeley, as a lecturer, teaching integrated-circuit design courses and working on an MOS delta-sigma modulator. In 1984, he joined the telecommunications design group of Silicon Systems Inc., Nevada City, CA. There he was involved in the design of three mixed-signal CMOS integrated circuits for voice-band modems, including the first single-chip 2400-bps modem, which was used initially in consumer voice-band modems and later in set-top boxes for billing. Since 1986, he has been on the faculty of the Department of Electrical and Computer Engineering at the University of California at Davis, where he is now a Professor. His research interests are in the area of analog and mixed-signal integrated-circuit design for signal processing and communication applications. His research projects have included work on data converters, filters, adaptive equalizers and timing recovery circuits for data communications, and image processing.

Professor Hurst was a member of the program committee for the Symposium on VLSI Circuits in 1994 and 1995 and a guest editor for the December 1999 issue of the IEEE Journal of Solid-State Circuits. He was a member of the program committee for the International Solid-State Circuits Conference from 1998-2001 and is now an associate editor for the IEEE Journal of Solid-State Circuits. He was elected Fellow of the IEEE in 2001. Professor Hurst taught (with Professor Richard Spencer) the short course *Signal Processing for Magnetic Recording* a dozen times. He is a co-author (with Professors Gray, Lewis and Meyer) of the fourth edition of the text book *Analysis and Design of Analog Integrated Circuits*. He is also active as a consultant to industry.