

UNIVERSITY OF CALIFORNIA, DAVIS
Department of Electrical and Computer Engineering

Electronic Circuits II
EEC 110B, Spring 2012

LECTURE: Tues, Thur 12:10-1:30pm in 106 Olson

LAB: Tu 2-5pm or Fri noon-3pm in 2157/2161 Kemper

INSTRUCTOR: P. Hurst

OFFICE HOURS: Tues 9:30-10:30am, Thur 1:30-2:30pm in 2031 Kemper

TEACHING ASSISTANTS: TBA (see class web page for names and office hours)

CLASS WEB PAGE:

<http://www.ece.ucdavis.edu/~hurst/EEC110b/>

TEXT: P. Gray, P. Hurst, S. Lewis and R. Meyer, "Analysis and Design of Analog Integrated Circuits," Wiley, 5th ed.

PREREQUISITES: EEC 110A

COURSE OBJECTIVE: This course will focus on analog bipolar integrated circuit design. A short introduction to the transistors and models will be followed by coverage of one stage amplifiers, current sources, the differential pair, cascaded amplifiers, op amps, frequency response, and feedback amplifiers.

There will be homework assignments, weekly labs, a midterm exam and a final exam. The homework assignments will not be graded.

The lab manual and homework assignments will be posted on the class web page.

GRADING: Lab 35%, Midterm 25%, Final 40%