## UNIVERSTIY OF CALIFORNIA, DAVIS Department of Electrical and Computer Engineering

## Electronic Circuits and Systems Engineering 100, Spring 2016

COURSE CONTENT: Introduction to the theory and application of analog and digital circuits and systems.

LECTURE: Tu, Th 12:10-1:00 pm in 1309 Surge 3

LAB: In 2157/2161 Kemper

**INSTRUCTOR: P. Hurst** 

OFFICE HOURS: Tu Th 9:30-10:30am in 2031 Kemper

TEACHING ASSISTANTS: A list of TA names and office hours will be posted on the E100 web page.

TEXT: L. Bobrow, "Fundamentals of Electrical Engineering", Oxford, Second Ed.

LABS: Will be posted on the E100 web page.

WEB PAGE: http://web.ece.ucdavis.edu/~hurst/E100

PREREQUISITES: Engineering 17

PREPARATION: The following material should be reviewed: Kirchoff's Laws, Thevenin and Norton equivalents, nodal and loop analysis, circuit response in frequency domain, complex numbers, and phasors. This is material covered in E17.

READING and HOMEWORK ASSIGNMENTS will be posted on the E100 web page.

HOMEWORK: Homework assignments will be made but will not be graded.

EXAMS: There will be two midterm exams in class. Tentative midterm dates: May 3 and 24. A comprehensive two-hour final exam will be given at the time scheduled for our course.

GRADING:

Lab	25%
Homework	0%
Midterm #1	20%
Midterm #2	20%
Final Exam	35%

COMMENTS ON THE LABORATORY: Each student must do each lab to pass the class. Attendance in lab is required. Work in groups of two. Each student must turn in a lab report. Each group will need one protoboard for building circuits (starting with lab 2). Information on buying protoboards is posted on the E100 web page.

E100 labs begin in April (starting 4/6). A protoboard is not needed for the first lab. The labs and the lab schedule appears on the class web page.