

**UNIVERSITY OF CALIFORNIA, DAVIS**  
**Department of Electrical and Computer Engineering**

**E100**

**Electronic Circuits and Systems**

**Spring 2016**

**ANNOUNCEMENTS:**

**1. LAB INFORMATION:**

Each student must submit a lab report for each lab. The 'lab report' will include prelab calculations, data collected in lab, plots and/or tables as requested, and answers to any questions. Write your name, your lab partner's name, your student ID number, and your lab day on your lab report. To assist the TAs in grading your lab report, label your report using the same section numbers and/or letters used in the lab handout. If there is a single result, enclose it in a *box*. Multiple data points or results should be presented graphically or in a table, whichever is most appropriate. NEATNESS counts, unreadable or hard-to-follow reports may be penalized.

Generally, lab reports are due at the beginning of the next lab period after you complete the lab. So the report for Lab 2 is due at the beginning of the lab period when you work on Lab 3. **If your lab is late** (i.e. not turned in at the start of a lab period), your lab score will be penalized 10% if it is turned in within 24 hours of the time it was due, and an additional 10% for each additional day that it is late. Lab reports more than 7 days late will not be graded but will be quickly looked over and given a maximum grade of 25% of the maximum possible score for that lab.

Contact your TA about how to turn in a late lab report.

Please note that there are separate prelabs for several labs. Some of the calculations in the prelabs are required before you can do the lab. **Do the prelab before each lab.**

**The labs and prelabs will be posted on the E100 web page.**

**2. PROBLEM SET SOLUTIONS:**

Problem set solutions will be posted on the E100 web site after the 'do' date.