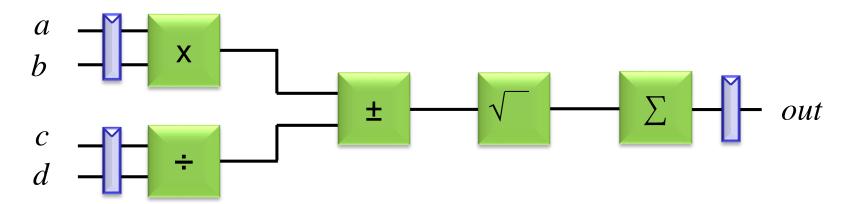
## **PIPELINING**

## Register → Comb. Logic → Register

 All paths in digital systems consist of an input register, (optionally) followed by combinational logic, followed by an output register

$$out = summation{sqrt[(a \times b) \pm (c / d)]}$$

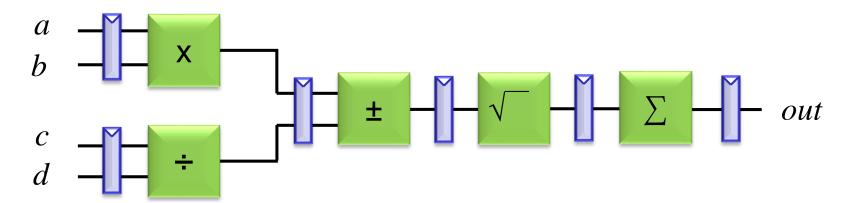


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## Adding Pipeline Stages

 Adding registers into combinational logic breaking the longest paths into shorter ones has the potential to permit the calculation of more operations per unit time—known as "pipelining"

$$out = summation{sqrt[(a \times b) \pm (c / d)]}$$



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