

Office Hour

Nov. 1

1) Clockless latches

- NAND
- NOR

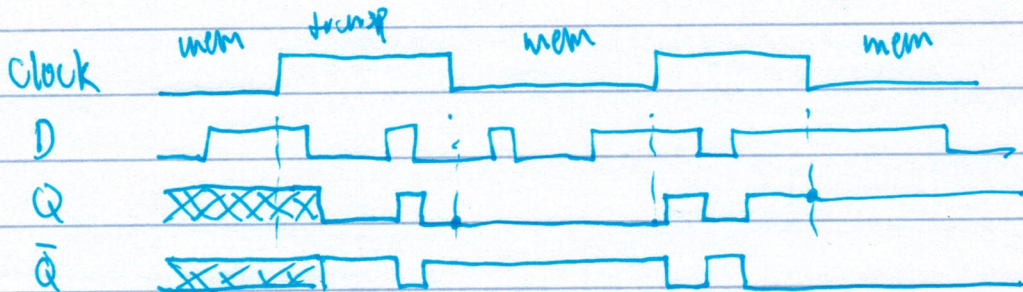
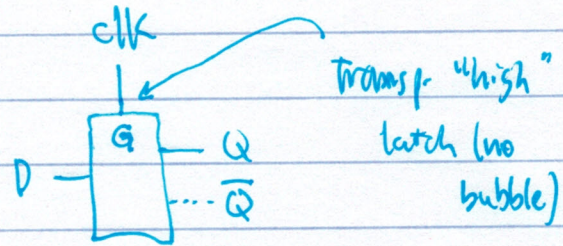
2) Transparent Latches

- SR *
- D *
- ⋮

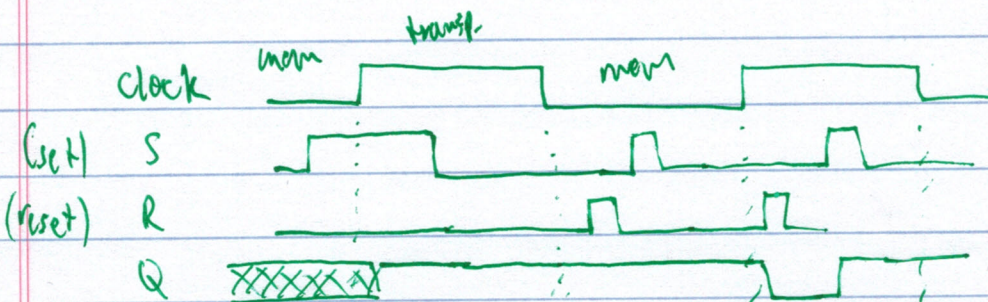
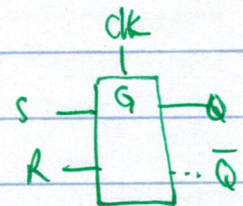
3) Flip-Flops (FF) "edge-triggered"

- D *

* D Transparent latch



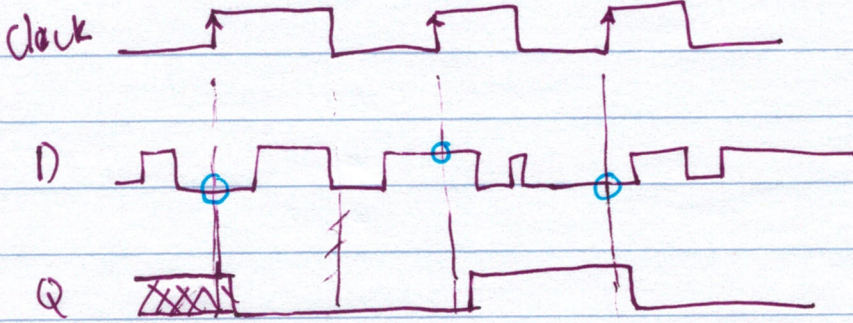
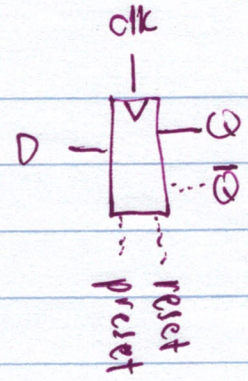
* SR Transparent latch



S	R	Q
0	0	Mem
0	1	0
1	0	1
1	1	- undefin

* DFF

D → Q on clock



positive-edge triggered FF

