

Sept. 30

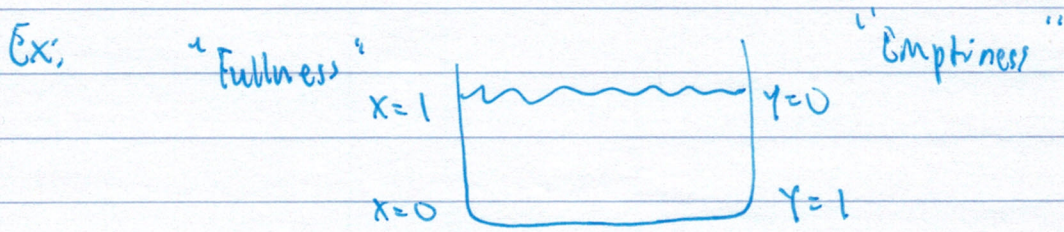
EEEC 18

1

Boolean Algebra

variables take on lot 2 values

- X = 1 or 0 EEC 18
- = ON or OFF
- = True or False Philosophy
- = 10V or 0V Circuits
- = Full or Empty



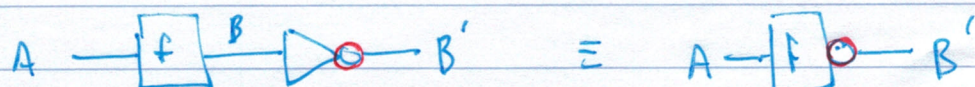
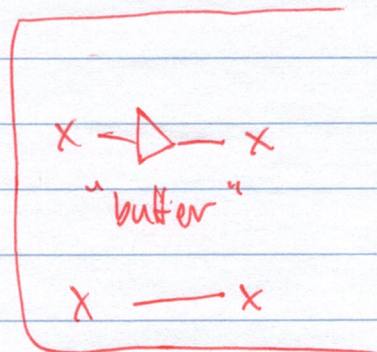
Basic Operations

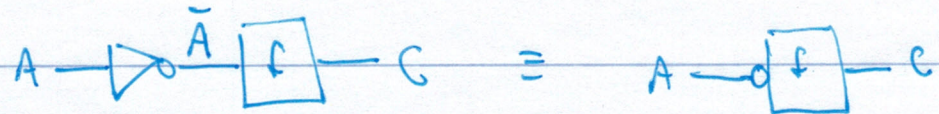
① Invert / NOT / complement

\bar{X} or X' or $!X$ or $\sim X$

If $X = \text{ON}$, $\bar{X} = \text{OFF}$

$Y = 0$, $Y' = 1$





Boolean variables have a limited number of possible values, so we can enumerate all cases in a Truth Table (T.T.)

inputs	outputs
X	X'
0	1
1	0

* Number of rows in a T.T. = 2^N , N = number of inputs

② AND operator

Two or more inputs:

X AND Y AND Z

$X \cdot Y \cdot Z$

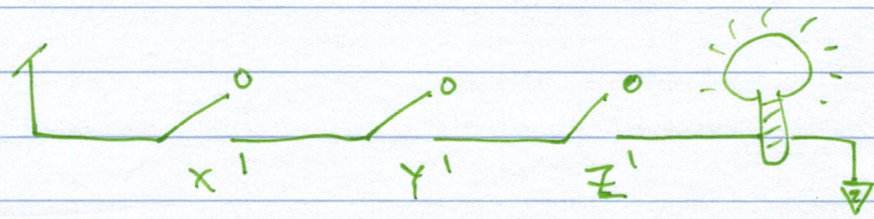
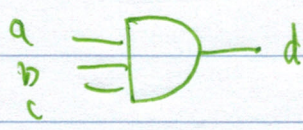
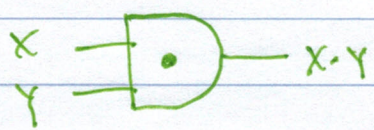
X & Y & Z

"Product" of X, Y, Z

Def: output is 1, only when all inputs are 1

X	Y	X · Y
0	0	0
0	1	0
1	0	0
1	1	1

X	Y	Z	X · Y · Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1



In "series"

③ OR operator

X OR Y OR Z

X + Y + Z

X | Y | Z

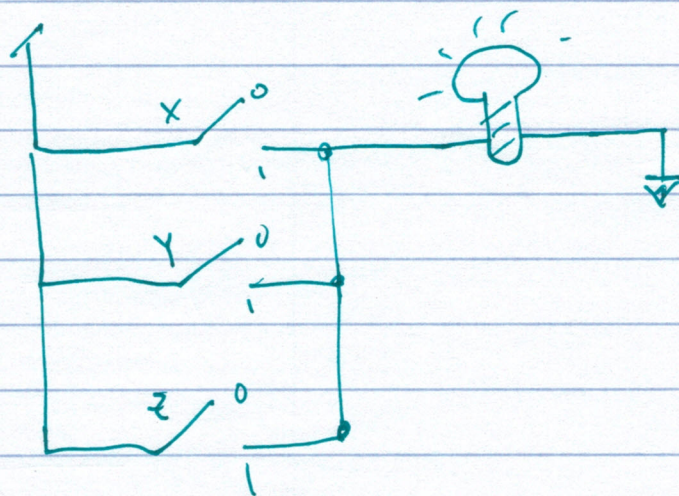
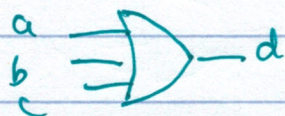
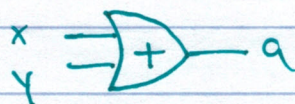
"Sum" of X, Y, Z

↑ logical "sum", not addition

Output is 1, if any input is 1

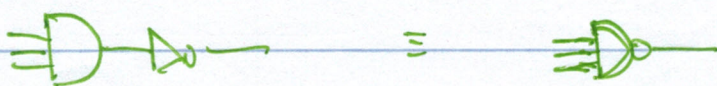
X	Y	X + Y
0	0	0
0	1	1
1	0	1
1	1	1

X	Y	Z	X Y Z
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

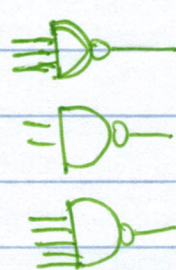


"parallel"

2a) NAND - NOT AND



Easy to make in CMOS

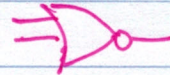


X	Y	$\overline{X \cdot Y}$
0	0	1
0	1	1
1	0	1
1	1	0

3c) NOR - NOT OR



=



X	Y	$\overline{X+Y}$
0	0	1
0	1	0
1	0	0
1	1	0

4) XOR, Exclusive OR

X XOR Y

$X \oplus Y$

$X \wedge Y$

output is 1 if one or the other input is 1, but not both

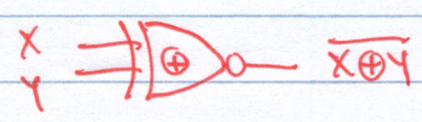
X	Y	$X \oplus Y$
0	0	0
0	1	1
1	0	1
1	1	0



Ex: switches at ends of a hall

$$\begin{array}{r} X \\ + Y \\ \hline \boxed{\text{Sum}} = X \oplus Y \end{array}$$

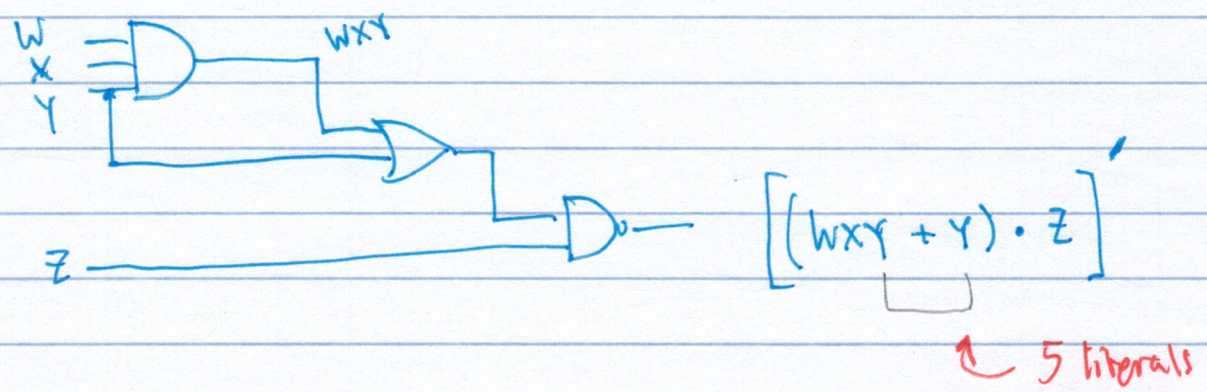
4a) XNOR, ~~Exclusive~~



X	Y	$\overline{X \oplus Y}$
0	0	1
0	1	0
1	0	0
1	1	1

XNOR or "Equivalence"

NOT, AND, OR - functionally complete



Literal Def: A variable or its complement in an expression

Theorems

Operations with 0, 1

$X + 0 = X$	} set to 1	XXXX
$X + 1 = 1$		
$X \cdot 0 = 0$	} clear to 0	OR 0001
$X \cdot 1 = X$		XXXX 1 set

XXXX
 AND 0011

 00XX cleared

Idempotent Laws

OR : $X + X = X$



X	Y	X+Y
→ 0	0	0
0	1	1 unused
1	0	1 unused
→ 1	1	1

AND: $X \cdot X = X$



Involution law "double chip"

$(X')' = \overline{\overline{X}} = X$

Laws of Complementarity

$X + X' = 1$

$X \cdot X' = 0$

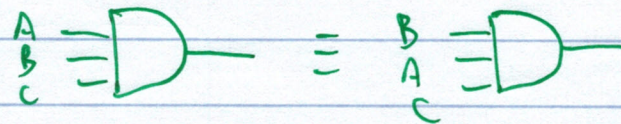
$(X+Y) \cdot (X+Y) = X+Y$

$(X'+Y \cdot Z') \cdot (X'+Y \cdot Z')' = 0$

Commutative law

$$\text{AND: } ABC = CBA = BAC \dots$$

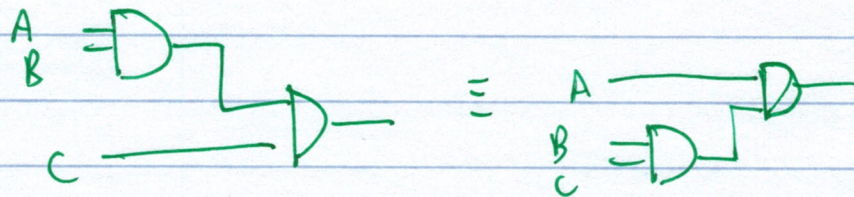
$$\text{OR: } A+B+C = C+B+A = B+A+C \dots$$



Associative Law

$$\text{AND: } (AB)C = A(BC) = ABC$$

$$\text{OR: } (A+B)+C = A+(B+C) = A+B+C$$



Distributive Law

$$\text{a) } A(B+C) = A \cdot B + A \cdot C$$

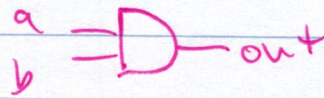
$$\text{b) } A + B \cdot C = (A+B) \cdot (A+C)$$

Helpful Theorems, ^{Roth} p-49

Ex: $X + XY = X$

Ex: $DC(KL' + M)(G + H) + C = C$

(Note: In the original image, green annotations show 'x' under 'DC' and 'Y' under '(KL'+M)', with a bracket connecting them to the 'x' under 'C' in the simplified equation.)



00	0
10	0
01	0
11	1

