

**AGENDA**  
**UNIVERSITY OF CALIFORNIA, DAVIS**  
**ELECTRICAL AND COMPUTER ENGINEERING**  
**INDUSTRIAL AFFILIATES CONFERENCE**  
**JANUARY 29, 2010**

<b>8:00 A.M. – 8:45 A.M.</b>	<b>CONTINENTAL BREAKFAST, REGISTRATION</b> Poster Session I - Kemper Hall Lobby
<b>8:45 A.M. – 8:50 A.M.</b>	<b>OPENING REMARKS - 1065 KEMPER HALL</b> PROFESSOR BRIAN KOLNER, INDUSTRIAL AFFILIATES COMMITTEE
<b>8:50 A.M. - 9:15 A.M.</b>	<b>CHAIR’S REPORT</b> ECE DEPARTMENT CHAIR, PROFESSOR RICHARD A. KIEHL
<b>9:15 A.M. – 10:00 A.M.</b>	<b>FACULTY PRESENTATION I - 1065 KEMPER HALL</b> PROFESSOR CHARLES HUNT <b>THE CHALLENGE OF HIGH-QUALITY, ENERGY-EFFICIENT LIGHTING</b>
<b>10:00 A.M. - 10:20 A.M.</b>	<b>COFFEE BREAK &amp; POSTER SESSION II -KEMPER HALL LOBBY</b>
<b>10:20 A.M. - 11:20 A.M.</b>	<b>GRADUATE STUDENT PRESENTATIONS I - 1065 KEMPER HALL</b> Justin Wenck: "Power Electronics, Memories, and Wireless Sensor Miniaturization" Senhua Huang: "Distributed Power Control for Cognitive User Access based on Primary Link Outage Feedback" Avinash P. Nayak: "Fabrication of Devices at Room Ambient on Arbitrary Substrates: Plastics, Glass, Fabrics, Rubber and Even Human Hair"
<b>11:20 A.M. - 12:20 P.M.</b>	<b>KEYNOTE SPEAKER - 1065 KEMPER HALL</b> Dr. Thomas N. Theis, Director, Physical Sciences, IBM Research, T.J. Watson Research Center <b>In Quest of the “Next Switch”: Prospects for Greatly Reduced Power Dissipation in a Successor to the Silicon Field Effect Transistor</b>
<b>12:20 P.M. - 1:30 P.M.</b>	<b>LUNCH – KEMPER HALL LOBBY/1127 KEMPER HALL</b>
<b>1:30 P.M. - 2:15 P.M.</b>	<b>FACULTY PRESENTATION II - 1065 KEMPER HALL</b> Professor Stephen O’Driscoll <b>Adaptive Circuit Design for Implantable Medical Devices</b>
<b>2:15 PM - 3:00 P.M.</b>	<b>LAB TOURS - KEMPER HALL LOBBY</b>
<b>3:00 PM – 3:20 P.M.</b> HALL LOBBY	<b>COFFEE BREAK &amp; VOTING FOR BEST POSTER – KEMPER</b>
<b>3:20 P.M. – 4:20 P.M.</b> HALL	<b>GRADUATE STUDENT PRESENTATIONS II - 1065 KEMPER</b>  Ramin Banan Sadeghian: "An Ultra-Selective Electronic Nose based on Field Ionization on Semiconducting Nanowire Arrays" Chi Law: "A High Gain 60 GHz Power Amplifier with 20 dBm Output Power in 90nm CMOS" Muhammad Zia: "Improving Bandwidth Efficiency of Hybrid ARQ under Orthogonal Space-Time Block Codes Without Diversity Loss"
<b>4:20 p.m. – 4:45 p.m.</b>	<b>Closing Remarks &amp; Best Poster Prize – 1065 Kemper Hall</b>
<b>5:00 P.M.</b> LOBBY	<b>RECEPTION WITH FACULTY &amp; STUDENTS – KEMPER HALL</b>